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September 18, 2003

Attorney Docket No. MIC-M082

Mail Stop Patent Application
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450



Enclosed herewith for filing is a patent application, as follows:

Inventor(s): Thomas S. Wong, Robert W. Bechdolt, Phi Thai
Title: Integrated Schottky Transistor Logic Configuration

<u>X</u>	Return Receipt Postcard
<u>X</u>	This Transmittal Letter
<u>9</u>	Page(s) Specification including Claims and Abstract
<u>6</u>	Sheet(s) of Drawings (Figs. 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, and 12)
<u>2</u>	Page(s) Declaration For Patent Application and Power of Attorney
<u>1</u>	Page(s) Recordation of Assignment Cover Sheet
<u>2</u>	Page(s) Assignment
<u>1</u>	Page(s) Form PTO-1449
<u>1</u>	Cited Reference

CLAIMS AS FILED

For	Number <u>Filed</u>		Number <u>Extra</u>		Rate		Basic Fee
Total Claims	15	-20 =	0	x	\$ 18.00 =	\$	750.00
						\$	0.00
Independent Claims	1	-3 =	0	x	\$ 84.00 =	\$	0.00
<input checked="" type="checkbox"/> Fee for Assignment Recordation						\$	40.00

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Respectfully submitted,

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